

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sanjay Dabral and Krishna Seshan

Serial No.

Filed: August 29, 2000

For: *DIODE AND TRANSISTOR DESIGN FOR
HIGH SPEED I/O*

Divisional Application of:

Serial No. 09/107,351

Filed: June 30, 1998

Examiner: Ortiz, E.

Art Unit 2815

#2/c
10/31/00
Amber

PRELIMINARY AMENDMENT

Box New Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In connection with the filing of the Divisional Application under Rule 1.53(b), Applicants respectfully request entry of the following amendment.

Please cancel claims 1-19.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN, LLP

Dated: 8/29/00

William T. Babbitt

William Thomas Babbitt, Reg. No. 39,591

12400 Wilshire Boulevard
Seventh Floor
Los Angeles, California 90025
(310) 207-3800

006280 " 53ET5960